

CYPR-CD01209M



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3-17-03

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :  
Craig NEMECEK : GROUP ART UNIT:  
SERIAL NO: 09/998,859 :  
FILED: NOVEMBER 15, 2001 : EXAMINER:

FOR: A SYSTEM AND A METHOD  
FOR CHECKING LOCK STEP  
CONSISTENCY BETWEEN AN IN  
CIRCUIT EMULATION AND A  
MICROCONTROLLER WHILE  
DEBUGGING PROCESS IS IN  
PROGRESS

RECEIVED  
MAR 10 2003  
TECHNOLOGY CENTER 2800

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following  
Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 10, line 12 with the following:

91  
A commercial ICE system utilizing the present invention is available from Cypress  
MicroSystems, Inc., for the CY8C25xxx/26xxx series of microcontrollers. Detailed information  
regarding this commercial product is available from Cypress MicroSystems, Inc., 22027 17th  
Avenue SE, Suite 201, Bothell, WA 98021, in the form of version 1.11 of "PSOC DESIGNER:  
Integrated Development Environment User Guide", which is hereby incorporated by reference.